Countdown Timer

CSE321 Fall 2021: Project 2

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# Introduction

# Specifications

# Features

# Applications

# Block Diagram

# Functionality Diagram

ASM, FSM State Diagram, or Flow Chart (Pick 1)

# BOM

# Schematic

The following set of schematics describes the set of electrical connections between the NUCLEO and peripheral devices required to create the timer.

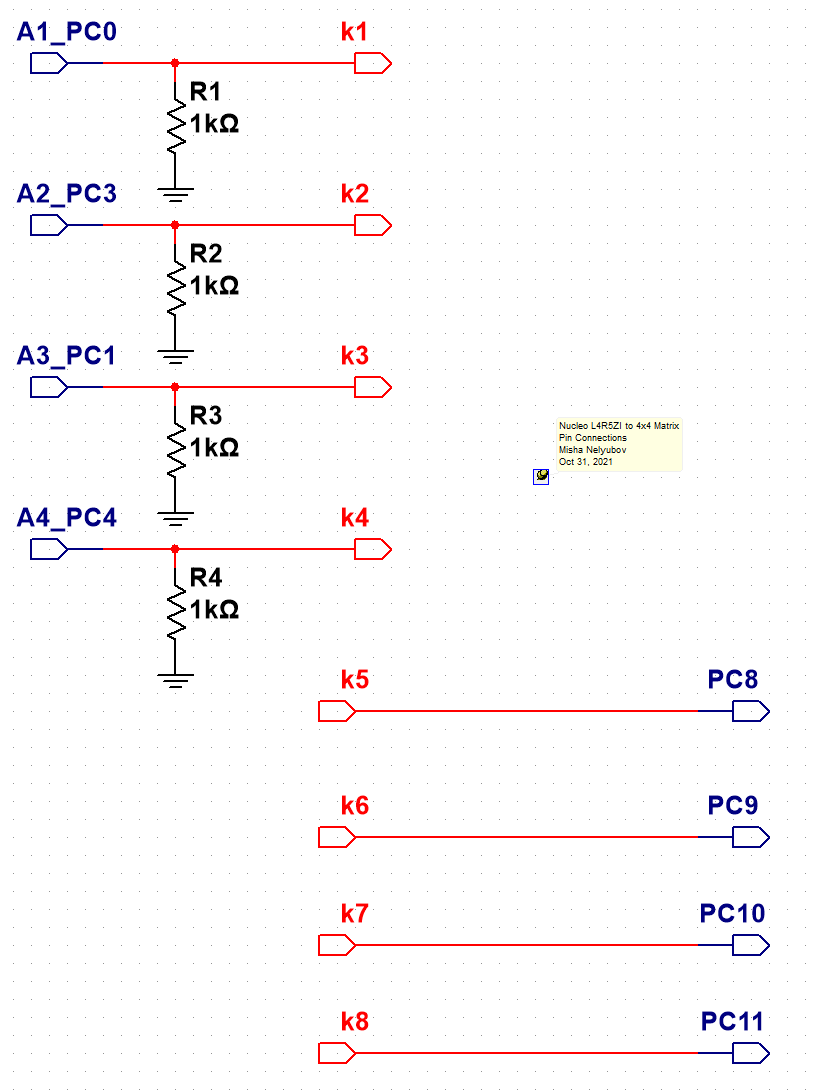


Figure 1. The eight pin connections between the NUCLEO microcontroller and 4x4 matrix keypad. Connectors k1-k8 represent pins 1-8 of the matrix keypad.



Figure 2. The four pin connections between the NUCLEO microcontroller and LCD.

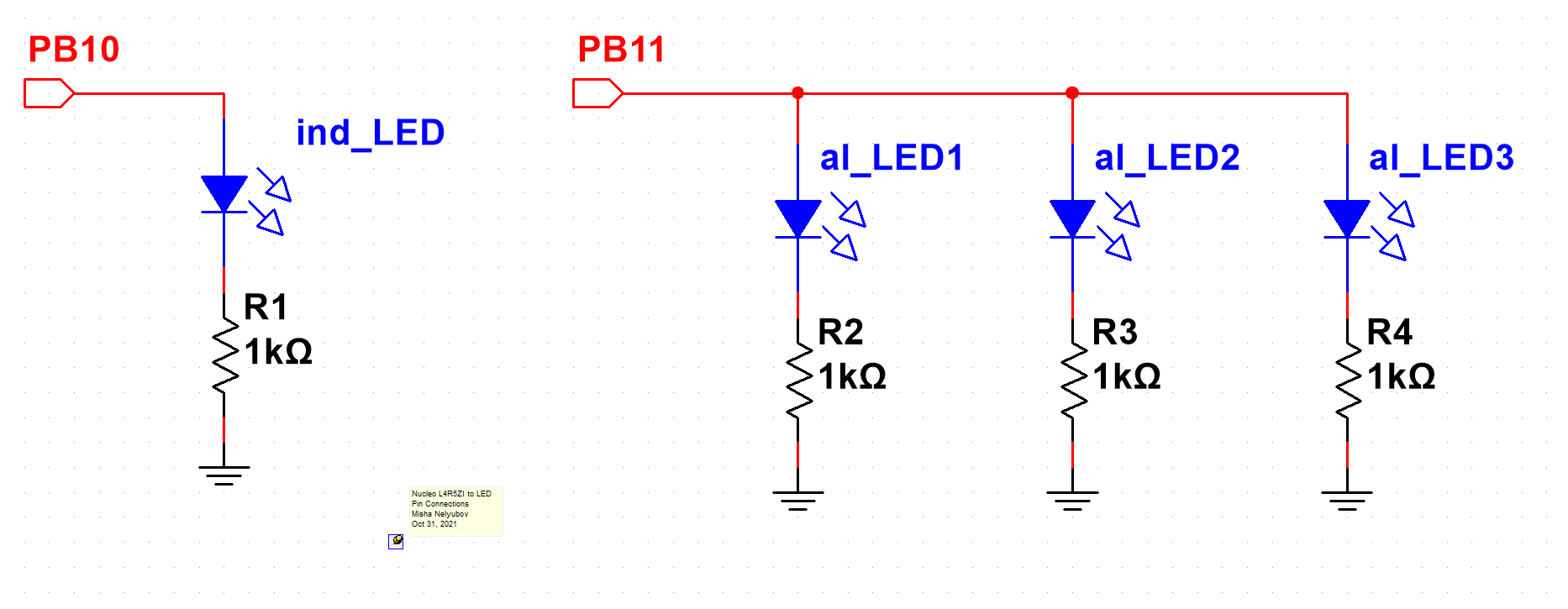


Figure 3. The two pin connections between the NUCLEO microcontroller and the indicator and alarm LEDs.

# Test Plan

# Results

# Recommendations for Improvement

* 1. Increase duration
  2. Add sound output when alarm sounds